

(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property Organization International Bureau



(43) International Publication Date
22 September 2005 (22.09.2005)

PCT

(10) International Publication Number
WO 2005/088830 A1

(51) International Patent Classification⁷:

H03F 1/02

(81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NA, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SM, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.

(21) International Application Number:

PCT/GB2005/000942

(22) International Filing Date: 11 March 2005 (11.03.2005)

(25) Filing Language:

English

(26) Publication Language:

English

(30) Priority Data:

0405724.6 13 March 2004 (13.03.2004) GB

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(84) Designated States (unless otherwise indicated, for every kind of regional protection available): ARIPO (BW, GH, GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IS, IT, LT, LU, MC, NL, PL, PT, RO, SE, SI, SK, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

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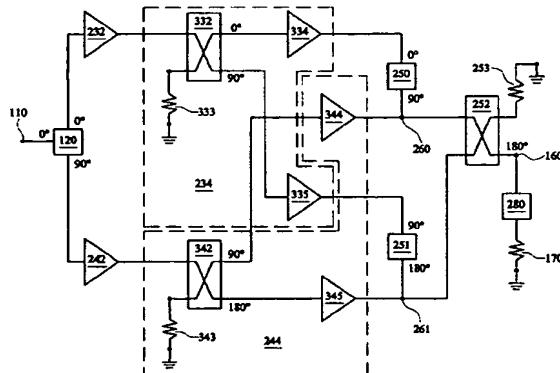
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Published:

- with international search report
- before the expiration of the time limit for amending the claims and to be republished in the event of receipt of amendments

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

(54) Title: A DOHERTY AMPLIFIER



(57) Abstract: A Doherty amplifier is described which comprises: a power splitter (120) having a first output and a second output, wherein the first output is connected to a main power splitter (392) having first and second outputs which differ in phase by 90°; and the second output is connected to an auxiliary power splitter (342) having first and second outputs which differ in phase by 90°; a main final stage amplifier (234) comprising first (324) and second (325) main paired amplifiers, the inputs of which are connected to the first and second outputs of the main power splitter (392); an auxiliary final stage amplifier (244) comprising first (344) and second auxiliary (345) paired amplifiers, the inputs of which are connected to the first and second outputs of the auxiliary power splitter (342); wherein the output from the first main paired amplifier (324) is connected to the output from the first auxiliary paired amplifier (344) by an impedance inverter (250); and the output from the second main paired amplifier (325) is connected to the output from the second auxiliary paired amplifier (345) by an impedance inverter (251); the amplifier being arranged such that a relative phase shift is introduced to the signals input to the main and auxiliary power splitters to offset the phase shift of the impedance inverters.

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